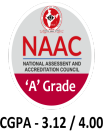




Savitribai Phule Shikshan Prasarak Mandal's  
**N B NAVALE SINHGAD COLLEGE OF ENGINEERING**  
*(Approved by AICTE & Affiliated to P. A. H. Solapur University, Solapur)*



**CURRICULAM VITAE**

	<p><b>Mr. URMILA SUNIL GATKUL</b>          ASST.PROF., M.E.(VLSI &amp; EMBEDDED SYSTEM)          ✉ <a href="mailto:urmimore@gmail.com">urmimore@gmail.com</a> ☎ <b>9284666438</b></p>			
<b>Profile Summary</b>	<ul style="list-style-type: none"> <li>To work in a challenging environment that provides generous opportunities for learning</li> </ul>			
<b>Educational Details</b>	<b>Institute / University Name</b>	<b>Passedout Year</b>	<b>Degree</b>	<b>Remark</b>
	Sinhgad Institute of Technology , Lonavala ,Pune University	2014	M.E.(VLSI & Embedded systems)	7.55 grade
	PICT-SITM Pune	2008	Post Graduate Diploma in VLSI	75.33.00%
	S.C.O.E. S.R.T.M.U. Nanded	2005	BE (Electronics)	65.06%
<b>Area Of Interest</b>	<ul style="list-style-type: none"> <li>VLSI And Embedded system</li> </ul>			
<b>Subjects Handled</b>				
<b>UG</b>	<ul style="list-style-type: none"> <li>1.VLSI 2.PRINCIPALS OF DIGITAL TECHNIQUES 3.BASIC ELECTRONICS 4.DIGITAL COMMUNICATION 5.MATLAB 6.ANALOG COMMUNICATION 7.DATA STRUCTURES 8.DATABASE MANAGEMENT SYSTEMS 9.COMPUTER ORGANISATION AND ARCHITECTURE 10. MICROPROCESSOR 11.APPLIED ELECTRONICS</li> </ul>			
<b>PG</b>	<ul style="list-style-type: none"> <li>VLSI</li> </ul>			
<b>Research And Guidance</b>				
<b>Total Journal Paper Publications</b>	2			
<b>Total Conference Paper Publications</b>	3			

<p><b>Detailed Journal Publication list</b></p>	<ol style="list-style-type: none"> <li>1. Paper Title: Complex DPLL using CORDIC Processor for I-Q channel Demodulator. Published in IJTEMAS(International Journal of latest technology in engineering management &amp; applied science) of electronics and communication engineering ,Volume: 3, Issue 4A On Apr.2014.</li> <li>2. Accident/RTO/Abandoned Vehicle Investigation Using QR Code on Vehicle Published in International Journal for Research in Applied Science &amp; Engineering Technology (IJRASET) ISSN: 2321-9653; IC Value: 45.98; SJ Impact Factor: 7.538 Volume 11 Issue VI Jun 2023- Available at www.ijraset.com</li> </ol>
<p><b>Detailed Conference Publication list</b></p>	<ol style="list-style-type: none"> <li>1. Urmila d.More, "DPLL by using CORDIC Processor using system programmable chip," Presented the paper in International Conference on "Science &amp; Technology(ICST-2K14)," Organized by,S.B.Patil College of Engineering, Indapur, Dist.Pune (M.S.,India) on 21-22 February 2014.</li> <li>2. Urmila d.More, " CORDIC Processor for complex DPLL using system programmable chip" Presented 3 the paper in " International Conference on Paradigm Shift Commerce,Engineering technology,Sociel Science(ICPSCETSS) Organized by , K.J.Somaiya College of Arts,Commerce &amp; Science ,Kopargaon,Dist-Ahmednagar (M.S., India) On 8 February 2014.</li> <li>3. URMILA S. Gatkul , "Accident/RTO/Abandoned Vehicle Investigation Using QR Code on Vehicle " Presented the paper in International conference on "Emerging trends in automation , communication &amp; security(ICETACS-2023)" Organised by N.B.Navale csinhgad college of engineering kegaon,solapur dist - solapur(M.S. INDIA)on20th and 21st July 2023</li> </ol>

**FDP/STTP/Workshop Attended: 13**

Sr.No	Title	Organized by	Dates from to	Duration in Weeks/ Months
1	Participated in Two days Workshop on "Programmable system on Chip " conducted by Cypress Semiconductors Pvt.Ltd.Bangalore,at Walchand Institute of Technology,solapur	Walchand Institute of Technology	2012-03-02 to 2012-03-03	2days
2	Participated in One day Workshop on " Test & Measument Equipment With New Technology" conducted by Scientech Pune	Sinhgad Institute of Technology,Lonavala	2012-02-17 to 2012-02-17	1day
3	Participated in One day Workshop on " Exploring Arduino Applications in Field of Engineering" Organized by Department of Electronics & Telecommunication,	SVERI's College of Engineering, Pandharpur	2017-06-29 to 2017-06-29	1 DAY
4	Participated in One Week STTP on "Internet of Things & It's Applications "	SVERI's College of Engineering, Pandharpur	2017-06-05 to 2017-06-11	
5	Participated in FDP on "Foundation	SVERI's College of Engineering,	2017-08-03 to	

6	Participated in Two Week FDP on "Power electronics for Renewable Energy Systems-Present trends and future scope,PERES-2017"	Chaitanya Bharathi Institute of Technology,Hydrabad	2017-11-13 to 2017-11-25	2 WEEK
7	Participated in One Week STTP on "Electronics Product Design	SVERI's College of Engineering, Pandharpur	2018-01-04 to 2018-01-08	1 WEEK
8	Participated in Three days Workshop on "User Interface Technologies " conducted by Infosys Limited	SVERI's College of Engineering, Pandharpur	2018-03-05 to 2018-03-07	3 DAYS
9	Participated in One Week National Level FDP on "Societal Applications of Machine Learning"	Shri Vishnu Engineering College for Women(A),Bhimavaram	2022-12-26 to 2022-12-30	1 WEEK
10	Participated in One Week Online STTP on "FOG EDGE COMPUTING & DATA SCIENCE"	byDr.D.Y.Patil Institute of Technology ,Pimpri,Pune in association with Buds Academy Hyderabad	2023-01-17 to 2023-01-21	1 WEEK
11	Participated in One day workshop on "RESEARCH FUNDING & PROPOSAL WRITING"	NBN Sinhgad college of Engineering kegaon Solapur	2023-02-11 to 2023-02-11	1 DAY
12	Participated in One Week FDP on "ELECTRIC VEHICLE"	NBN Sinhgad college of Engineering kegaon Solapur	2023-02-27 to 2023-03-03	1 WEEK
13	Participated in Two days workshop on "Deploying and Running Your Application Server"	NBN Sinhgad college of Engineering kegaon Solapur	2023-03-24 to 2023-03-26	3 DAYS

### Other Activities

<b>Professional Memberships</b>	1. The Institution of Engineering and Technology, India
<b>Major Projects Handled</b>	
<b>Personal Details</b>	
<b>Employee Code</b>	SOL/KG/
<b>Adhar Card Number</b>	347655858350
<b>Pan Card Number</b>	BCUPM4525M